REMARKS

Applicant appreciates the Examiner's thorough consideration with respect to the present application. Claims 1, 3-7, 9, and 11-20 are currently pending. Claims 1, 4-7, 9, 11 and 12 have been amended. Claims 1, 6, 9, 11 and 13 are independent. Claim 8 has been cancelled. Entry of the above amendments is earnestly solicited. Reconsideration of this application, as amended, is respectfully requested.

Allowable Subject Matter

Applicant appreciates the Examiner's indication of allowable subject matter. Specifically, claims 13-20 have been allowed. Claims 8, 9 and 11 have been objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Without conceding the propriety of the Examiner's rejections, but merely to expedite the prosecution of the present application, claims 1, 6, 9, 11 and 13 have each been amended to include allowable subject matter. Accordingly, all of the claims of the present application should be allowed.

Specifically, claim 1 includes the limitations of original claims 1, 6 and 8 (now cancelled). Claim 6 includes the limitations of original claims 1, 4, 6 and 9.

Claim 9 includes the limitations of original claims 1, 6 and 9. Claim 11 includes the limitations of original claims 1, 4 and 11.

Claim Rejections Under 35 U.S.C. § 102

Claims 1-3, 5 and 6 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Kleinberg (U.S. Patent No. 4,862,114). This rejection is respectfully traversed.

In light of the foregoing amendments to the claims, Applicant submits that this rejection has been obviated and/or rendered moot. As indicated by the Examiner, the prior art of record fails to teach or suggest the unique combination of elements of claims 1, 3 and 6. Accordingly, these rejections should be withdrawn.

Claim Rejections Under 35 U.S.C. § 103

Claims 4, 7 and 12 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Kleinberg (U.S. Patent No. 4,862,114). This rejection is respectfully traversed.

In light of the foregoing amendments to the claims, Applicant submits that this rejection has been obviated and/or rendered moot. As indicated by the Examiner, the prior art of record fails to teach or suggest the unique

combination of elements of 4, 7 and 12. Accordingly, these rejections should be withdrawn.

Applicant respectfully submits that the prior art of record, either in combination together or standing alone, fails to teach or suggest the invention as is set forth by the claims of the instant application. Accordingly, reconsideration and withdrawal of the claim rejection are respectfully requested.

As to the dependent claims, Applicant respectfully submits that these claims are allowable due to their dependence upon an allowable independent claim, as well as for additional limitations provided by these claims.

CONCLUSION

All the stated grounds of rejection have been properly traversed and/or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider all presently pending rejections and that they be withdrawn.

It is believed that a full and complete response has been made to the Office Action, and that as such, the Examiner is respectfully requested to send the application to Issue.

Attached hereto is a marked-up version of the changes made to the application by this Amendment.

In the event there are any matters remaining in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.

Pursuant to the provisions of 37 C.F.R. §§ 1.17 and 1.136(a), the Applicant petitioned for an extension of one (1) month to September 24, 2002 for the period in which to file a response to the Office Action dated May 24, 2002 in the concurrently filed Request for Continued Examination (RCE). The required fee has been paid in connection with the proper filing of this Request for Continued Examination (RCE).

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

Marc S. Weiner
Reg. No. 32,181

P. O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

MSW/MTS/cl
0234-0415P

Attachment: Version with Markings to Show Changes Made

MARKED-UP VERSION OF THE AMENDMENTS

IN THE CLAIMS:

Claim 8 has been cancelled.

The claims have been amended as follows:

1. (Twice Amended) A high-frequency oscillation circuit comprising:

a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, said at least one logic element including a first logic element within said closed loop circuit;

another logic element external to said closed loop circuit;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor and having a basic oscillation frequency of between 1 MHz to 500 MHz.

4. (Amended) A high-frequency oscillation circuit as claimed in claim 1, wherein said first logic element includes a high-speed TTL or CMOS.

5. (Twice Amended) A high-frequency oscillation circuit as claimed in claim 1, wherein said crystal oscillator of high frequency [having] has a basic oscillation frequency of at least 30 MHz [or more].

6. (Twice Amended) A high-frequency oscillation circuit comprising: [A high-frequency oscillation circuit as claimed in claim 1,]

a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, wherein said at least one logic element [including] includes a first logic element within said closed loop circuit and another logic element external to and in serial connection with said closed loop circuit, said first logic element including a high speed CMOS or a high speed TTL;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor.

7. (Twice Amended) A high-frequency oscillation circuit as claimed in claim [6] 1, wherein said first logic element includes a high-speed CMOS.

9. (Twice Amended) A high-frequency oscillation circuit comprising:

a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, said at least one logic element including a first logic element within said closed loop circuit;

another logic element external to said closed loop circuit;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor, [A high-frequency oscillation circuit as claimed in claim 6,] wherein said crystal oscillator is [used as] a sensor element for chemical measurement of a predetermined parameter.

11. (Amended) A high-frequency oscillation circuit comprising:

a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, wherein said logic element includes a high-speed TTL or CMOS;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor, [A high-frequency oscillation circuit as claimed in claim 4,] said crystal oscillator having a basic oscillation frequency of 500 MHz or more.

12. (Amended) A high-frequency oscillation circuit as claimed in claim 1, [further comprising] wherein said another logic element being disposed externally to and in serial connection with said closed loop circuit.